



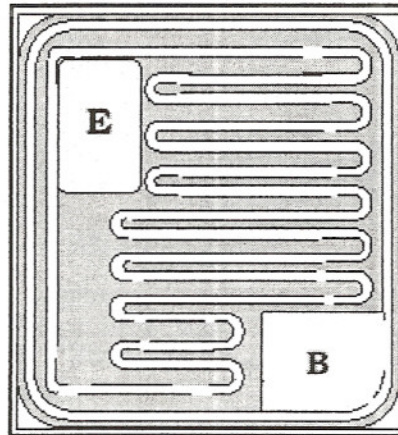
Sierra Components, Inc.

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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

99 mils



93 mils

Topside Metal: Al
Backside: AuAsNiAg
Backside Potential: Collector
Mask Ref:
Bond Pads (Mils): E=18x30,B=20x26

APPROVED BY:
MFG: Fairchild

DIE SIZE (Mils): .099"x.093"
THICKNESS: .008"

DATE: 4/5/04
P/N: D44H8